

<b>Notice of Allowability</b>	Application No.	Applicant(s)
	10/809,682	TAKAISHI, MASARU
	Examiner Asok K. Sarkar	Art Unit 2891

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTO-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1.  This communication is responsive to amendment filed 5/5/2006.
2.  The allowed claim(s) is/are 6-8 and 10-15.
3.  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a)  All
  - b)  Some\*
  - c)  None
 of the:
  1.  Certified copies of the priority documents have been received.
  2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3.  Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4.  A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5.  CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a)  including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1)  hereto or 2)  to Paper No./Mail Date \_\_\_\_\_.
  - (b)  including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6.  DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1.  Notice of References Cited (PTO-892)
2.  Notice of Draftperson's Patent Drawing Review (PTO-948)
3.  Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date \_\_\_\_\_
4.  Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5.  Notice of Informal Patent Application (PTO-152)
6.  Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_
7.  Examiner's Amendment/Comment
8.  Examiner's Statement of Reasons for Allowance
9.  Other \_\_\_\_\_.

**DETAILED ACTION**

***Response to Arguments***

1. Applicant's explanation of the instant invention in pointing the difference with the cited prior art was found to be persuasive.

***Allowable Subject Matter***

2. Claims 6 – 8 and 10 – 15 are now allowed.
3. The following is an examiner's statement of reasons for allowance:

Claims 6, 8, 10 and 15 recite, *inter alia*, a semiconductor device comprising a plurality of cells each including a drain region of a first conductivity type, channel region of a second conductivity type different from the first conductivity type and a source region of the first conductivity type stacked in this order on a semiconductor substrate so as to be capable of forming a channel in a direction of a thickness of the semiconductor substrate and a low resistance region of the second conductivity type having a conductivity higher than that of the channel region, the low resistance region forming a part of an inner wall of a hole formed between adjacent ones of the plurality of cells, the low resistance region extending in an isotropic manner with respect to a predetermined region in the hole so as to be in contact with the channel region, wherein the drain region is shared by the plurality of the cells and wherein the low resistance region and the drain region are in contact with each other. The art of record does not disclose or anticipate the above limitation in combination with other claim elements nor would it be obvious to modify the art of record so as to form a device including the above limitation.

Claims 7 and 11 – 14 recite, *inter alia*, a semiconductor device comprising a plurality of cells each including a drain region of a first conductivity type, channel region of a second conductivity type different from the first conductivity type and a source region of the first conductivity type stacked in this order on a semiconductor substrate so as to be capable of forming a channel in a direction of a thickness of the semiconductor substrate and a low resistance region of the second conductivity type having a conductivity higher than that of the channel region, the low resistance region forming a part of an inner wall of a hole formed between adjacent ones of the plurality of cells and being in contact with the channel region, wherein the drain region is shared by the plurality of the cells and wherein the width of the cell is smaller than 2 micron. The art of record does not disclose or anticipate the above limitation in combination with other claim elements nor would it be obvious to modify the art of record so as to form a device including the above limitation.

***Conclusion***

4. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."
5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Asok K. Sarkar whose telephone number is 571 272 1970. The examiner can normally be reached on Monday - Friday (8 AM- 5 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, William B. Baumeister can be reached on 571 272 1722. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

6. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

*Asok Kumar Sarkar*

Asok K. Sarkar  
May 22, 2006

Primary Examiner